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~~"The case for the reduced instruction set computer," Computer Architecture News 8:6 (October), 25-33. Google Scholar Digital Library Patterson, D. A., and J. L. Hennessy [2004].~~

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~~Thank you Prof. Hennessy and Patterson, as well as all other contributors for writing such an approachable book, not only for students, but also for practitioners. This edition brings the book up to date with the developments in computer architecture and various surrounding technologies, such as memory, disk, etc. The GPU chapter was fun to read.~~

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